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FAX COVER SHEET

June 25, 2004

Page 1 of 3

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Lattice Ref.: L03-055

Re: Attorney Docket No. 1054.030

In re application of:

Howard Tang, Satwant Singh, and Ann Wu

Serial No.: Filed:

10/809,658 03/25/04

Group No.:

Examiner:

For:

Configuring FPGAs and the Like Using One or More Serial Memory Devices

TRANSMITTAL OF ERRORS IN FILING RECEIPT

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In the title, please replace "Cofiguring" with -Configuring-.

Changes are noted on the accompanying copy of the Filing Receipt.

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Respectfully submitted,

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Applicant(s)

Howard Tang, Cupertino, CA; Satwant Singh, Fremont, CA; Ann Wu, San Jose, CA;

Assignment For Published Patent Application

Lattice Semiconductor Corporation, Hillsboro, OR;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 06/10/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

CONFIGURING

Cofiguring FPGAs and the like using one or more serial memory devices